



ASSISTANT PROFESSOR  
Manakula Vinayagar Institute of Technology,  
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**P.SATHIYANARAYANAN**

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**Professional Objective:**

To explore, understand and share knowledge on a broad spectrum of technologies related to computer science with the student community to update myself along with them.

**Career profile:**

To pursue an academic career by working as a teacher to benefit the students and thereby the institution by adopting various teaching techniques.

**Educational qualification:**

M.E./M.Tech: M.Tech. CSE from Pondicherry University, Puducherry in 2013.

B.E./BTech : B.Tech. CSE from Perunthalaivar Kamarajar Institute of Engineering and Technology, Karaikal in 2011.

**Work experience: (starting from present position)**

S.No	Employer	Designation	Period of Service	Nature of Work
1	Manakula Vinayagar Institute of Technology	Assistant Professor	July 2013- Till Date	Handling both Theory and Laboratory classes. Dept. Exam Cell Coordinator, Conduction of Value Added Courses

**Teaching Activities: (Subjects Taught)**

**(i) Under Graduate :**

Object Oriented Programming  
Object Oriented Programming and Design  
Computer Networks  
Wireless Communication and Networks  
Artificial Intelligence  
Grid Computing

**(ii) Laboratories handled:**

Object Oriented Programming Lab  
Computer Networks Lab

**Major tasks handled:**

Conducted Value Added Course on Python Programming  
Guided students in coding competitions  
Acted as Mentor for Hackathon-2017

**Project Guidance:**

No. of B.E. Projects Guided: 07

**Professional Membership:**

ISTE Chapter (*Membership No: LM 108594*)  
IAENG Member (*Membership No: 142373*)

**Paper Publications / Presentation:**

	Published	Accepted	Communicated	Total
International Journal	6			6
National Journal	1			1
International Conference	-			-
National Conference	-			-

**FDP/STTP/Workshops attended:**

S. No	Type of the Programme	Organized By	Period (From – To)
1.	Attended a 5-Days FDP on “Data Science and Big Data Analytics” by ICTACP	Rajiv Gandhi College of Engineering & Technology, Puducherry	05-09 June 2017
	Attended a 3-Days FDP on “Python Programming” sponsored by CSI	St.Joseph’s Institute of Technology	31 May – 02 June 2017
2.	Attended a Two Days National Seminar on “IPR Awareness & Research Methodology”	Karpaga Vinayaga College of Engg. & Tech., Madhuranthagam	27-28 May 2016
3.	Attended a FDP on “Web Designing and Scripting (JavaScript and DHTML)”	ICT Training Academy, Puducherry	24-28 November 2015
4.	Participated in One Day Faculty Development Program on “Life Skills”	ICT Training Academy, Puducherry	02-06 November 2015
5.	Participated in One day Faculty Development Programme on “Knowledge Management”	TCS – Siruseri, Chennai	06 July 2015

S. No	Type of the Programme	Organized By	Period (From – To)
6.	Participated in One day Workshop on “Research and Development in Engineering Institutions” sponsored by ISTE	Manakula Vinayagar Institute of Technology, Puducherry	13 September 2014
7.	Participated in National Seminar on “Impact of Biometric Security using Image Processing”	Manakula Vinayagar Institute of Technology, Puducherry	12-13 November 2013
8.	Participated in Workshop on “Cyber Security and Malware Analysis”	Manakula Vinayagar Institute of Technology, Puducherry	2-3 August 2013
9.	Participated in a 8-Days Faculty Development Programme on “Project Management with .Net Technologies” by ICTACP	Achariya College of Engineering & Technology, Puducherry	26 August 2013 – 5 September 2013
10.	Participated in Workshop on “Android Application Development (Basic & Advanced)”	National Institute of Technology, Trichy	1-3 March 2013
11.	Participated in Workshop on “Network Security and Hacking”	Pondicherry University, Puducherry	2 March 2012
12.	Participated in Inter-National Seminar on “Psycho-Social Factors in Child Sexual Abuse”	Sri Kanyaka Parameswari Arts & Science College for Women, Chennai	28-29 February 2012
13.	Participated in National Seminar on “Number Theory and Cryptography”	Pondicherry University, Puducherry	30-31 January 2012

#### **International Journal:**

1. Published a paper titled “*Expressive and deployable two factor secure key exposure protection using hybrid cryptosystem*” in International Journal on Electronics & Wireless Communication Engineering with ISSN: 2454-9924 , Volume 10 Issue 10 – March 2018.
2. Published a paper titled “*Predicting user Service Evaluation from Mobile user in Geological Location*” in International Journal of Engineering Science and Computing with ISSN: 2321-3361, Volume 7, Issue 4, April 2017. *Page No: 6512-6515*
3. Published a paper titled “*Enhancement of Link Stability using RDGR in VANET*” in International Research Journal of Engineering and Technology (IRJET) with e-ISSN: 2395-0056 p-ISSN: 2395-0072, Volume 3, Issue 3, March 2016. *Page No: 572- 577*
4. Published a paper titled “*Privacy preserving access control mechanism using accuracy constrained framework for relational data*” in International Journal of Emerging Technology in Computer Science and Electronics with ISSN: 0976-1353, Volume 13, Issue 3, March 2015. *Page No:37-40*

5. Published a paper titled “*Alternative approach of Handling DOS Vs Mobility Management with Effective Utilization of Client’s Needs*” in International Journal of Innovative Research in Computer Science & Technology with ISSN: 2347-5552, Volume 3, Issue 1, January 2015. *Page No: 6-8*
6. Published a paper titled “*Maturity Model for Analyzing Risks in Cots Components*” in International Journal of Research and Scientific Innovation (IJRSI) with ISSN: 2321 – 2705, Volume I, Issue I, June 2014. *Page No: 57-66*

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**National Journal:**

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1. Published a paper titled “*A Reliable Communication Scheme for VANET Communication Environments*” in Indian Journal of Science and Technology with ISSN (Print): 0974-6846 ISSN (Online): 0974-5645, Vol 7(S5), 31–36, June 2014. *Page No: 31-36*