



G.SHARMILA

Assistant Professor/ECE
Manakula Vinayagar Institute of
Technology, Madagadipet,
Puducherry, Pin code – 605 107.
E mail: sharmilaece@mvit.edu.in
Mobile: 9442519190

Professional Objective:

To work in a reputed organization, in a responsible position where my skills will be effectively utilized for mutual progression.

Career profile:

Educational qualification:

M.Tech : Electronics and communication Engineering in Manakula Vinayagar Institute of technology, Pondicherry University in the year 2014.

B.E : Electronics and communication Engineering in Mailam Engineering College, Anna University in the year 2011.

Work experience: (starting from present position)

S.No	Employer	Designation	Period of Service	Nature of Work
1	Manakula Vinayagar Institute of technology	Assistant Professor	2 years	Teaching, Regular
2	Dr.Navalar Nedunchezhiyan College of Engineering	Assistant Professor	2 years	Teaching, Regular

Teaching Activities: (Subjects Taught)

(i) Under Graduate :

Digital Image Processing
Microprocessor and Microcontroller
Digital Electronics
Electronic devices and circuits
Cellular Mobile communication
Cryptography and Network Security

Digital Communication
Communication engineering
Medical Electronics

(ii) **Post Graduate:**
Advanced Digital signal Processing

(iii) **Laboratories handled:**

Digital electronics Lab
Electronic circuits-II circuits and simulation lab
Analog and Digital circuits lab
Microprocessor and Microcontroller Lab
Communication-II Lab
Advanced Communication Lab

Consultancy and Sponsored Research activities:

Name of the Scheme	Project Title	Duration	Amount Sanctioned	Status Completed/* Ongoing
	-			

Major tasks handled:

- Coordinator of International conference on Emerging trends in engineering at Dr.Navalar Nedunchezhiyan College of Engineering in the year 2016.
- Department Alumni coordinator in Manakula Vinayagar Institute of technology since June 2016.
- Conducted MATLAB Workshop for the students.
- Class Advisor and student mentor.
- Staff in charge of Department News Letter and Magazine.

Paper Publications / Presentation:

	Published	Accepted	Communicated	Total
International Journal	2			2
National Journal	1			1

International Conference	5	1		6
National Conference	4			4

FDP/STTP/Workshops attended:

- ❖ Attended Short Term Training Programme on Embeddd IOT & IOE in MVIT, Puducherry from 11th to 15th Dec 2017.
- ❖ Attended a FDP titled on Emotional Intelligence at Rajiv Gandhi College of Engineering and Technology Engineering, Puducherry on 23rd & 24th Oct 2017 sponsored by ICT Academy.
- ❖ Attended Hands-on Workshop on Python and Machine Learning at PEC on 5th & 6th Feb 2018.
- ❖ Completed NPTEL online certificate course on “Digital Image Processing” during Mar to Apr 2016.
- ❖ Attended 2 days FDP titled “Solar Energy – Technology & Products” on 9th & 10th June at MVIT, Phocos India Solar Pvt Ltd, Pondicherry
- ❖ Attended 2 days Workshop on “Understanding Mathematics in Engineering Perspective” at MVIT conducted by ECE department in association with FLASHE and Mathematical club during Dec 2016.
- ❖ Attended 10 days workshop on “Photoshop” at MVIT conducted by CSE department in the month of Dec 2016
- ❖ Attended 2 days FDP on “Introduction to Mobile APP Development” conducted by ICT Academy of Tamil Nadu on 29th & 30th June 2016.
- ❖ Attended 3 days FDP on “NI graphical system design platform for industrial IOT application” on 23rd to 25th March, 2017.
- ❖ Participated in two days FDP program on Teaching Methodology held at Dr.NNCE, Tholudur.
- ❖ Attended one day FDP program on cloud computing and Big data analysis conducted by ICT Academy held at SRM hotel, Thiruchy
- ❖ Attended a three days workshop on “Neural Network, Fuzzy Logic and Pattern Recognition” from 27-29th Sep 2013 held at IEEE, MSRIT students chapter, Bangalore.
- ❖ Attended a two days workshop on Matlab for “Communication Engineering” during 25th to 26th Jan 2014 at MIT, Puducherry.

- ❖ Participated in three days workshop on “Advanced Image Processing & Pattern recognition Techniques” during 2nd to 4th Sep 2013 at MIT, Puducherry.
- ❖ Attended a two day workshop on “Analysis and Simulation of Self Organized Networks” using NS2 held on 8th& 9th Feb 2013 at MIT, Puducherry.
- ❖ Attended a two day workshop on “Digital Image Processing” using Matlab held on 1st& 2nd Mar 2013 at MIT, Puducherry.
- ❖ Participated in the workshop on “Embedded System” during the National Level Students Technical Symposium held during 2nd to 4th Sep, 2010 at Pondicherry Engineering College, Puducherry.
- ❖ Participated in “DSP and PCB” at EUPRAXIA 2010 conducted by SSN College of Engineering, Chennai.

Project Guidance:

About 8 projects were guided for UG students in the area of Image processing and embedded system