



**N.ELAKKIYA**

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**Manakula Vinayagar Institute of**  
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**Professional Objective:**

To be an astute learner and the best performer in your organization. So that I can build an innovative career in your esteemed organization by using my skills and other significant talents.

**Career profile:**

**Educational qualification:**

M.E./M.Tech : M.E (VLSI DESIGN), at Velalar College of Engineering and Technology, Erode by 2013 with 7.85 CGPA

B.E./B/Tech : B.E (ECE), at Karpagam College of Engineering, Coimbatore by 2010 with 73.66 %

**Work experience: (starting from present position)**

| S.No | Employer  | Designation         | Period of Service          | Nature of Work |
|------|---|---------------------|----------------------------|----------------|
| 1    | Manakula Vinayagar<br>Institute of<br>Technology,Puducherry | Assistant Professor | 04.06.2015 to<br>Till Date | Regular        |
| 2    | Sri Subramaniya<br>College of Engineering<br>& Technology   | Assistant Professor | 2 years                    | Regular        |

**Teaching Activities: (Subjects Taught)**

**(i) Under Graduate :**

Electronic Devices and Circuits  
Data Communication and Networks  
VLSI Design  
Medical Electronics  
Digital Image Processing

**(ii) Laboratories handled:**

Digital Circuits Laboratory  
Electronic Devices and Circuits Laboratory

**Major tasks handled:**

- Ward Mentor
- Handled Special Lecture
- Coordinated Guest Lecture
- Mini Project Co-Ordinator
- Other Department Activities
- Active member of the organizing team for two national Conferences.

**Paper Publications / Presentation:**

|                          | <b>Published</b> | <b>Accepted</b> | <b>Communicated</b> | <b>Total</b> |
|--------------------------|------------------|-----------------|---------------------|--------------|
| International Journal    | 2                | 2               | -                   | 2            |
| National Journal         | -                | -               | -                   | -            |
| International Conference | 2                | 2               | -                   | 2            |
| National Conference      | 4                | 4               | -                   | 4            |

**FDP/STTP/Workshops attended:**

- Attended a one day National level workshop on “MEMS” on 2nd August, 2008.
- Attended a two days workshop on “Measurement Techniques for Laboratory Equipments” on 5-6 December, 2011 at Velalar College of Engineering and Technology.
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- Attended a two days workshop on “Measurement Techniques for Laboratory Equipments” on 5-6 December, 2011 at Velalar College of Engineering and Technology.
- Attended a five days National level workshop on “Roadmap Towards 5G Wireless Infrastructure”.
- Attended a 3 days Faculty Development Program conducted by ICT Training Academy, Pudhucherry.
- Attended Comprehensive Hands on Training Program Using Embedded System and Development.

**Project Guidance :**

1. A Current Leakage Detection Circuit For Dram with Sense Amplifier

**Professional Membership:**

- Life Member in ISTE(Indian Society for Technical Education).