



RAJESH.V

*Assistant professor
Manakula Vinayagar Institute of
Technology, Madagadipet,
Puducherry, Pin code – 605 107.
E mail: rajeshce@mvit.edu.in
Phone:-
Mobile: 9894184602*

Professional Objective:

To contribute myself to the given opportunity effectively and to take part in all the endeavors, towards the success of the concern being innovative and flexible.

Educational qualification:

M.E in Applied Electronics at Sathyabama University, Chennai.

B.Tech in Electrical and Electronics Engineering at Sri Manakula Vinayagar Engineering College, Madagadipet, Puducherry.

Work experience: (starting from present position)

S.No	Employer	Designation	Period of Service	Nature of Work
1	Manakula Vinayagar Institute of Tech., Puducherry.	Assistant Professor	6 years	Regular
2	RMK Systems Ltd, Chennai	Design Engineer	1 years 3 Months	Research and Development of Electronic Project
3	HCL Info Systems Ltd	Engineer Trainee in R&D	1 Years 7 Months	Hardware testing, PCB preparation BOM Preparation

Teaching Activities: (Subjects Taught)

- (i) **Under Graduate :**
1. VLSI Design
 2. Embedded Systems

3. Digital Image Processing
4. Microprocessor and Microcontroller
5. System Design Using IC's
6. Digital Circuits
7. Telecommunication and Switching Networks
8. Basic Electrical and Electronics Engineering

(ii) Post Graduate :

1. Embedded Core Design
2. VLSI System Design

(iii) Laboratories handled:

1. Microprocessor and Microcontroller Lab
2. System Design Using IC's Lab
3. Embedded Systems Lab
4. Digital Circuits Lab

Major tasks handled:

- Technical Club Department Coordinator since June 2016.
Have been conducting various events and competitions for the students as a part of technical club activities.
- Department Association Coordinator since June 2016.
Conducted workshops, Poster presentations, Guest lecturers for the benefit of the students.
- MITILENCE'18 Coordinator
National Level Symposium conducted for students to participate in technical and non technical events.
- Conducted following workshops
 - i. MICROWIND
 - ii. LTSPICE
 - iii. QCA DESIGNER
 - iv. PCB DESIGN
- Coordinated NBA work in department level.
- Session incharge in IEEE International Conference ICSCAN 2018.

Paper Publications / Presentation:

	Published	Accepted	Communicated	Total
International Journal	4	-	-	4
National Journal	-	-	-	-
International Conference	5	-	-	5
National Conference	3			3

FDP/STTP/Workshops attended:

1. Attended Five days Short Term Training Programme On Embedded IoT & IoE held at MVIT on Dec 2017.
2. Attended Three days FDP on NI Graphical System Design Platform for IoT APP onducted by ICTACT of Tamilnadu Held on March 2017.
3. Attended Two Days FDP on "INRODUCTION TO MOBILE APP DEVELOPMENT" conducted by ICTACT of Tamilnadu.
4. Attended Two Days FDP on " Solar Energy" Conducted by PHOCOS India Pvt Ltd
5. Participated in Two days Faculty Development programme on 'Physical Design of Analog and Digital Circuit using cadence Design Suite' at SRM University on Feburary 2015.
6. Participated in Two Days workshop on 'Introduction to Robotics' at Pondicherry Engineering College on March 2015

Project Guidance: UG and PG Project guidance**Professional Membership:**

- Life Member – Indian Society for Technical Education (LM ISTE).
- Life Member – International Association of Engineers (IAENG)