



Vivek. K

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Professional Objective:

Seeking a position as a lecturer in your esteemed institution wherein I can utilize my knowledge, skills and experience to impart quality education to the students.

Educational qualification:

Ph.D : -

M.E./M.Tech : **M.Tech in VLSI Design** (First Class with Distinction) from SASTRA University, Tanjore with a CGPA of **8.4603**

B.E./B/Tech : **B.Tech in Electronics and Communication Engineering** (First Class) from Rajiv Gandhi College of Engineering and Technology (affiliated to Pondicherry University) with **71.6%**

Work experience: (starting from present position)

S.No	Employer	Designation	Period of Service	Nature of Work
1	IFET College of Engineering	Assistant Professor		Teaching and mentoring

Teaching Activities: (Subjects Taught)

- (i) **Under Graduate :** Wireless Networks
Signals and Systems
Digital Signal Processing
Communication Theory
Microprocessors and Microcontrollers
Electronics and Microprocessor
- (ii) **Laboratories handled:** Digital Signal Processing
Network and Transmission Lab

FDP/STTP/Workshops attended:

- Participated in an FDP on **Quality Circles** held at IFET College of Engineering
- Participated in an FDP on **Team Building** held at IFET College of Engineering
- Participated in an FDP entitled **Orientation for Fresh Faculty** held at IFET College of Engineering
- Participated in an FDP entitled **Visual C Programming** conducted by the Centre for Faculty Development, Anna University Chennai held at IFET College of Engineering

Project Guidance : UG – 9 Projects
PG - 4 Projects

Professional Membership: LM of ISTE