



**ASSISTANT PROFESSOR  
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**R.MUTHUNAGAI**

**Professional Objective:**

To enter in a reputed organization and to serve as an ASSISTANT PROFESSOR in the respective field also to extend my horizon towards the development of the institution with high multitasking ability to balance teaching and administrative duties.

**Career profile:**

Exceptionally seasoned and dedicated Engineering ASSISTANT PROFESSOR with a record in both teaching and administration. Mentoring and communicating effectively with students from diverse background and have the strong ability to deliver course material through a variety of teaching methods.

**Educational qualification:**

**POST GRADUATE : (M.Tech – Power Electronics and Drives)**

Sri Manakula Vinayagar Engineering College, Madagadipet,  
Pondicherry.  
[2012-2014], Pondicherry University.

**UNDER GRADUATE : (B.Tech - Electrical and Electronics Engineering)**

Sri Manakula Vinayagar Engineering College, Madagadipet,  
Pondicherry.  
[2000-2004], Pondicherry University.

**Work experience: (starting from present position)**

S.No	Employer	Designation	Period of Service	Nature of Work
1	Manakula Vinayagar Institute Of Technology, Puducherry	Assistant Professor	4 Years (From July 2014 to till date)	Teaching
2.	Achariya College of Engineering & Technology, Puducherry	Project Associate	1 Year	Teaching

3.	Sri Jayaram Engineering College, Cuddalore.	Lecturer	3 Years	Teaching
4	Pondicherry Electricity Board, Puducherry	Graduate Engineer Trainee	1 Year	Web Page Design (HTML), Intranet Design, Operation & Maintenance

### Teaching Activities: (Subjects Taught)

#### (i) Under Graduate :

1. Basic Electrical & Electronics Engineering
2. Electrical Electronics Engineering
3. Electrical Machines-II
4. Electrical Engineering
5. Electronic Devices and Circuits
6. Transmission & Distribution
7. Energy Engineering
8. Solid State Devices
9. Special Electrical Machines
10. Power Quality
11. Bio Medial Engineering
12. Bussiness Management
13. Electrical Safety and Quality Management

#### (ii) Laboratories handled:

1. Basic Electrical and Electronics Engineering Lab
2. Electrical Engineering Lab
3. Electronics I Lab
4. Electrical Machines-I
5. Electrical Machines-II

### Paper Publications / Presentation: 7

	Published	Accepted	Communicated	Total
International Journal	4	-	1	5
National Journal	Nil	-	-	Nil
International Conference	1	-	-	1
National Conference	1	-	-	1

**FDP/STTP/Workshops attended: 13**

**Reviewer for the journals: Nil**

**Research guidance: Nil**

**Project Guidance : 8**

**Guest Lectures delivered: Nil**

**Professional Membership: Life Member of ISTE**

### **ANNEXURE**

#### **Paper Publications / Presentation: [ 7]**

1. Dr.S.Anbumalar, S.Deepika and R.Muthunagai, “Design of Hybrid Renewable Energy System for Reduction of Harmonics using Artificial Intelligence and Optimization Technique” in International Journal of Science, Engineering and Technology Research , Volume 3.Issue 3,March-2014,pp.430.-435
2. R.Muthunagai, and D.Raja, “RPWM based Four Switch Three phase inverter fed induction motor drive with MPC Controller” in International journal of Innovative Research and studies (IJIRS) (Journal Published)
3. R.Muthunagai, D.Muruganandhan and R.UmaMaheswari, “Cost Effective B4 Inverter fed Induction Motor Drive” International Journal of Novel Research in Electrical and Mechanical Engineering Vol. 3, Issue 2, pp: (1-11), Month: August 2016(Journal Published)
4. D.Muruganandhan, S.Rajkumar and R.Muthunagai, “Voltage Sag Mitigation Scheme based on Power Quality Using Dynamic Voltage Restorer” published in IJAER journal under Vol.10 issue no.51. (Journal Published)
5. R.Muthunagai, “Performance Investigation of Self-lift and Re-lift Luo Converter Topologies” in International Conference on SMART (Systems, Methodologies, Automation and Research Trends.
6. R.Muthunagai, “Solar Thermal Storage and Replacement of Silicon Solar Cell” in the 5<sup>th</sup> National Conference on ICTEEE’17 at Mailam Engineering College on 22 March 2017.
7. R.Muthunagai, D.Muruganandhan, “Hardware Implementation of an Improved Current Control Scheme for Brushless Dc Motor”, International Journal of Electronics Engineering Research (IJEER), (Communicated)/Web of science.

### **FDP/STTP/Workshops attended: [13]**

1. Participated in one day workshop on Programmable Logic Controller(PLC) at CHEMFLUENCE'14, a National Level Technical Symposium held at AC Tech, Anna University, Chennai on 25th February, 2014
2. Participated in 2 days FDP on "Solar Energy Technology and Products" conducted by PHOCOS solar India private limited on 9<sup>th</sup> and 10<sup>th</sup> June
3. Participated three days FDP on "E-Literacy Level II"(Advanced Teaching Techniques)conducted by ICT Training Academy (DIT), Pondicherry
4. ISTE Sponsored one day hands on training on "EMBEDDED SYSTEM" organized by EEE department (NEXGEN Technologies)
5. Participated one day national workshop on "Writing Scientific Research Paper" organized by SSN college of Engineering, Chennai.
6. One day National Workshop on "Industry 4.0" conducted by PLCC centre, Pondicherry.
7. One day skill development program in MVIT Titled "Testing and Maintenance Of Transformers And Motors" Conducted by National Power Training Institute, Neyveli.
8. Participation in CSIR Sponsored two days workshop on "Recent Trends In Hybrid Power Generation" at MIT from 31.06.2017 to 01.07.2017.
9. Attended a one day FDP on "Electro Magnetic Transient Program (EMTP-RV) for power System Applications" at AMET University 22.09.2017, Chennai.
10. Participated one day workshop titled "Energy conservation and its methodology" on 19th Nov 2017 organized by Puducherry HR Circle & MVIT.
11. Participated ISTE one week STTP titled "Contemporary World of Electricity - Solar Energy" on 27th Nov to 1st Dec 2017 Organized by MVIT.
12. Attended AICTE sponsored one day seminar on "Electric Drives and Challenges of Electric transportation" in collaboration with Indian Railways & ABB on 12<sup>th</sup> December 2017 at KCG college of Technology.
13. CSIR sponsored one day workshop on "Challenges of optimization algorithm on solving and modeling of Real time & OFF line Engg problems" at IFET.

### **Name of the Project guided :[8]**

1. Harmonic Reduction using Various Randomized PWM Technique.
2. Performance Investigation of Self-lift and Re-lift Luo Converter Topologies.
3. Bridgeless Natural Resonant AC-DC Converter with Power Factor Correction.
4. Closed Loop PWM Control for Induction Motor Drive Using Dual Output Three Phase Inverter.
5. Current Limit Strategy for BLDC Motor Drive with Minimized DC-Link Capacitor.
6. Single Power Conversion AC-DC Converter with high power factor and high efficiency.
7. Implementation of Sliding mode Controller plus Proportional Triple Integral Controller for Negative Output Elementary Boost Converter.
8. Performance Evaluation for Speed Control of Induction Motor using Hybrid Controller.