



**Assistant Professor**  
**Department of E.E.E**  
**Manakula Vinayagar Institute of**  
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## **Mr.D.Muruganandhan**

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### **Professional Objective:**

A challenging position that seeks an ambitious and career conscious person, where acquired technical skills will be utilized towards diverse job responsibilities, continued growth and contribution to the institution success. Committed to offering interested students my full attention and expertise to help further their academic and professional goals.

### **Career profile:**

- To teach and educate students with widespread extent of experience in engineering instruction at the college level
- To teach and educate students according to the educational needs and abilities, also with Superior creative and critical thinking abilities.

### **Educational qualification:**

#### **POST GRADUATE- M.Tech**

**June 2010**

Electrical Drives And Control

Pondicherry Engineering College, Puducherry,

Pondicherry University, Puducherry. India.

#### **UNDER GRADUATE – B.Tech**

**May 2008**

Electrical And Electronics Engineering

Sri Manakula Vinayagar college of Engineering,

Pondicherry University, Puducherry, India.

### **Work experience: (starting from present position)**

<b>S.No</b>	<b>Employer</b>	<b>Designation</b>	<b>Period of Service</b>	<b>Nature of Work</b>
1.	Manakula Vinayagar Institute Of Technology	Assistant Professor	16-05-12 To till date	Teaching and Research
2.	Dr.S.J.S.Pauls Memorial college of Engineering	Assistant Professor	21-05-2010 To 15-05-2012	Teaching and Research

**Teaching Activities: (Subjects Taught)**

- (i) **Under Graduate** :Electronics Circuits,  
 Linear Control Systems,  
 Utilization of Electrical Energy,  
 Measurement and Instrumentation,  
 Electronics Devices and Circuits,  
 Power System Restructuring and Deregulation,  
 Power System Analysis,  
 Transmission and Distribution,  
 Energy Engineering,  
 Pulse and Digital Circuits,  
 Digital Circuits,  
 Basic Electrical and Electronics Engineering
- (ii) **Post Graduate** : -- Nil --
- (iii) **Laboratories handled:** Basic Electrical & Electronics Laboratory  
 Measurement and Control Lab  
 Electronics -I lab  
 Electronics -II lab  
 Electronics -III lab  
 Digital circuits lab  
 Power System Simulation Lab  
 Project Phase – I

**Paper Publications / Presentation:**

	<b>Published</b>	<b>Accepted</b>	<b>Communicated</b>	<b>Total</b>
International Journal	6	-	-	6
National Journal	-Nil-	-	-	-Nil-
International Conference	02	-	-	02
National Conference	01	-	-	01

**FDP/STTP/Workshops attended: 18 (Annexure Attached)****Reviewer for the journals: -Nil-****Research guidance: -Nil-****Project Guidance: 9****Guest Lectures delivered: NIL****Professional Membership: Life Member of ISTE**

## ANNEXURE

### **Paper Publications / Presentation: [ 8]**

1. D.Muruganandhan, S.Rajkumar and R.Muthunagai, “Voltage Sag Mitigation Scheme based on Power Quality Using Dynamic Voltage Restorer” published in IJAER journal under Vol.10 issue no.51. (Journal Published)
2. R.Muthunagai, D.Muruganandhan and R.UmaMaheswari, “Cost Effective B4 Inverter fed Induction Motor Drive” International Journal of Novel Research in Electrical and Mechanical Engineering Vol. 3, Issue 2, pp: (1-11), Month: August 2016(Journal Published)
3. R.Muthunagai, D.Muruganandhan, “Hardware Implementation of an Improved Current Control Scheme for Brushless Dc Motor”, International Journal of Electronics Engineering Research (IJEER), (Communicated)/Web of science.
4. “Comparison of F1 and F2 failures of MLI using amplitude Limited Modulation Technique” published in IRJET journal under Vol.3 issue no.11.
5. “Gradient Descent Algorithm Based Harmonic Elimination for AC to AC Converter” International Conference on Innovative Research in Engineering (ICIRE 2015)
6. “Harmonic Elimination in Nine Level Inverter by Imperialist Competitive Algorithm” International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 3, Issue 5, May 2014.
7. Presented a paper titled ”Compensation of voltage sag and swell to improve power quality using Dynamic voltage Restorer”, at the International Conference on Intelligent Design Analysis of Engineering Products, Systems and Computation(IDAEPSC-10),at Sri Krishna College of Engineering and Technology, Coimbatore held on July 9-10,2010.
8. Enhancement of Energy storage in Lithium Polymer Battery with nano fillers for capacitor application.

### **FDP/STTP/Workshops attended: [13]**

1. Participated in 2 days FDP on “Solar Energy Technology and Products” conducted by PHOCOS solar India private limited on 9<sup>th</sup> and 10<sup>th</sup> June
2. Participated three days FDP on “E-Literacy Level II”(Advanced Teaching Techniques)conducted by ICT Training Academy (DIT), Pondicherry
3. ISTE Sponsored one day hands on training on “EMBEDDED SYSTEM” organized by EEE department (NEXGEN Technologies)
4. Participated one day national workshop on “Writing Scientific Research Paper” organized by SSN college of Engineering, Chennai.
5. One day skill development program in MVIT Titled “Testing and Maintenance Of Transformers And Motors” Conducted by National Power Training Institute, Neyveli.
6. Participation in CSIR Sponsored two days workshop on “Recent Trends In Hybrid Power Generation” at MIT from 31.06.2017 to 01.07.2017.
7. Attended a one day FDP on “Electro Magnetic Transient Program (EMTP-RV) for power System Applications” at AMET University 22.09.2017, Chennai.

8. Participated one day workshop titled "Energy conservation and its methodology" on 19th Nov 2017 organized by Puducherry HR Circle & MVIT.
9. Participated ISTE one week STTP titled "Contemporary World of Electricity - Solar Energy" on 27th Nov to 1st Dec 2017 Organized by MVIT.
10. CSIR sponsored one day workshop on "Challenges of optimization algorithm on solving and modeling of Real time & OFF line Engg. problems" at IFET.
11. AICTE Sponsored SDP on "Alternative Energy system for Near Future "on 21<sup>st</sup> June to 2<sup>nd</sup> July 2012 at SMVEC.
12. AICTE Sponsored Two weeks FDP on "Analysis and modeling of motors using FEM package [ANSYS] on 18<sup>th</sup> Nov to 30<sup>th</sup> Nov 2013 at MIT.
13. Participation in one day seminar on "Research & Development in Engineering Institutions" in MIT.
14. Participated in Three days Faculty Development Program on E-Literacy Level II (Advanced Teaching Techniques) at ICTACT, Pudhucherry.
15. Participated one day seminar on Accreditation –Norms, Standards, Implementation In Technical Education Dr.Alagumurthy, Professor, Pondicherry engineering college 4<sup>th</sup> June 2016.
16. Participation in CSIR Sponsored two days workshop on "Recent Trends In Hybrid Power Generation" at MIT from 31.06.2017 to 01.07.2017.
17. Participated ISTE one week STTP titled "Contemporary World of Electricity - Solar Energy" on 27th Nov to 1st Dec 2017 Organized by MVIT.
18. Participated in MSME sponsored two days workshop on "Conceptual Practices in Industrial Automation" at AMET University 13<sup>th</sup> and 14<sup>th</sup> Dec,2017.

**Project guided: [8]**

1. Interleaved PFC buck boost converter for power balance control in wind energy conversion.
2. Mitigation of Voltage sag using DVR
3. Investigation On Dielectric And Thermal Properties On HDPE-Alumina Composite For Capacitor Application.
4. Implemtation Of Mppt Algorithm Based Fuzzy Logic For Solar PV System
5. Enhancement of Energy storage in Lithium Polymer Battery with nano fillers for capacitor application
6. Investigation on Blended Polymer material with Alumina Nano fillers for Capacitor application
7. voltage sag mitigation by using UPFC
8. Investigation On Polypropylene With Strontium Titanate Nanofiller For Electrical Applications
9. Power consumption reduction in air conditioner.