

**V.THEBINAA** 

Assistant Professor Manakula Vinayagar Institute of Technology, Madagadipet, Puducherry, Pin code – 605 107. E mail: thebinaaeee@mvit.edu.in

Phone: Nil

Mobile: +91-95973 70676

# PROFESSIONAL OBJECTIVE:

To work with a reputed institution as an Assistant professor, that will provide me a good platform to utilize my teaching & administration skills and will help me to grow my career by providing students with a rigorous and challenging education, along with the confidence, tools and skills required to build and advance toward a framework of success.

#### **CAREER PROFILE:**

• To be successful learners and works to create a classroom atmosphere that is stimulating, encouraging, and adaptive to the varied needs of students.

## **EDUCATIONAL QUALIFICATION:**

#### **POST GRADUATE- M.E**

Specialization: Power system engineering

Institute: Annamalai University. Year of completion: May-2012

#### **UNDER GRADUATE – B.E.**

Major: Electrical and Electronics Engineering

Institute: Annamalai University. Year of completion: May-2008

#### **WORK EXPERIENCE:** (starting from present position)

Sl.No	Employer	Designation	Period of	Nature of
			Service	Work
01	Manakula Vinayagar	Assistant Professor	From June2012	Teaching and
	Institute Of		to till date vbf	other
	Technology,			curricular
	Puducherry.			activities
02	Thiruvalluvar college	Lecturer	From March 2009	Teaching and
	of Engineering and		to April 2010	other
	Technology,			curricular
	Thiruvannamalai			activities

## **TEACHING ACTIVITIES:** (Subjects Taught)

- (i) Under Graduate:
  - 1. Power system analysis
  - 2. Utilization of electrical energy
  - 3. Transmission and distribution
  - 4. Linear control system
  - 5. Linear and Digital control system
  - 6. Electrical Machines II
  - 7. Circuit theory
  - 8. Basics Electrical and Electronics Engineering
  - 9. Electrical Engineering
  - 10. High Voltage Engineering
  - 11. Basic Electrical and Electronics Engineering
- (ii) Post Graduate: Nil(ii) Laboratories handled:
  - 1. Basic Electrical and Electronics Engineering Lab
  - 2. Electrical Engineering Lab
  - 3. Electrical Machines lab
  - 4. Power Electronics Lab

# PAPER PUBLICATIONS / PRESENTATION:

Journal/Conference	Published	Accepted	Communicated	Total
International	06	-	01	07
Journal				
National Journal	-	-	-	-
International	01	-	-	01
Conference				
National	-	-	-	-
Conference				

FDP/STTP/WORKSHOPS ATTENDED:	10
REVIEWER FOR THE JOURNALS:	Nil
RESEARCH GUIDANCE:	Nil
PROJECT GUIDANCE:	06
GUEST LECTURES DELIVERED:	Nil
PROFESSIONAL MEMBERSHIP:	Life Member of ISTE

#### **Publications**

- Presented a Paper in a International conference on futuristic trends in Electronics Engineering titled as 'Non Linear control scheme for speed regulation of DC drive' ICFTEE-2012
- ❖ "Speed Regulation of DC drive using Variable Structure Controller" in International Journal of Engineering Research & Technology under the title" in volume 2, Issue 5 of May 2013.
- \* "Hopfiled Neural Network based Selective Harmonic Elimination methods for H bridge Inverter" in IJEET- International Journal of Electrical Engineering & Technology in volume 4, issue 4 of July August, 2013.
- ❖ "GA based Performance Enhancement of SHE methods for Nine Level Inverter" in IJETTCS International Journal of Emerging Trends & Technology in Computer Science in volume 2, issue 5 of November 2013.
- \* "Estimate the Switching angle by using Imperialist Competitive Algorithm of multilevel inverter" in IJAREEIT- International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering in Volume 3, Issue 5. May 2014.
- \* "The comparison of F1,F2 Failures of MLI using amplitude limited modulation" in International research journal of engineering and Technology, Vol. 3 Issue 11, November 2016, ISSN (Online) 2395 -0056.
- ❖ "Fault tolerant cascaded H bridge multilevel inverter by reconfiguration of modulation" in Advances in Natural and Applied Sciences, Special Issue ,May 2017, ISSN (Online) 1995-077

## Faculty Development Program and Workshop

- ❖ One Day workshop on "National Programme on Technology Enhanced Learning (NPTEL)" held at Manakula Vinayagar Institute of technology, Puducherry on 21<sup>st</sup> December 2012.
- ❖ AICTE Sponsored two weeks faculty development programme on "Analysis and modelling of motors using FEM Package (ANSYS) organised by the Department of Electrical and Electronics Engineering held at Manakula Vinayagar Institute of technology, Puducherry from 18<sup>th</sup> − 30<sup>th</sup> November 2013.

- ❖ Participated two days faculty devolpment programme on "solar energy –technology & products" 9th -10<sup>th</sup> june 2016 organized by PHOCOS, Puducherry.
- ❖ Participated three days Professional development workshop, titled "TEACH Transform Empower and Change" on 15th June to 17th June 2017 organized by MVIT.
- ❖ Participated CSIR two days Seminar titled "Recent Trends in Hybrid Power Generation" on 31st June & 1st July 2017 organized by MVIT.
- ❖ Participated AICTE Sponsored FDP titled "Smart Grids and Green Systems (Smart Greens)" on 5th Oct & 6th Oct 2017 organized by PEC.
- A Participated one day workshop titled "Energy conservation and its methodology" on 19th Nov 2017 organized by Puducherry HR Circle & MVIT.
- ❖ Participated ISTE one week STTP titled "Contemporary World of Electricity Solar Energy" on 27th Nov to 1st Dec 2017 Organized by MVIT.
- ❖ Participated two days national workshop, titled "CRIO for Renewable Energy Applications" on 2nd & 3rd Dec 2017 organized by centre for renewable energy, Annamalai University.