



MANAKULA VINAYAGAR INSTITUTE OF TECHNOLOGY



An Autonomous Institution

Affiliated to Pondicherry University, Approved by AICTE, New Delhi,

Accredited by NAAC with 'A' Grade

Kalitheerthalkuppam, Puducherry- 605 107.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of 3rd Board of Studies meeting

The third Board of studies meeting of Department of Electrical and Electronics Engineering was held on 25th July 2026 at 10:30 am through Hybrid mode.

The following members were present for the BoS meeting

Sl.No	Name of the Member	Designation
Head of the Department (Chairman)		
1.	Dr.C. Shanmugasundaram Professor & HoD Department of EEE Manakula Vinayagar Institute of Technology Puducherry	Chairman
The entire faculty of each specialization		
2.	Dr. K.Sedhuraman, Associate Professor Dept of EEE Specialization: Power Electronics and Drives Manakula Vinayagar Institute of Technology	Member
3.	Mr. S.Rajkumar, Assistant Professor (SG) Dept of EEE Specialization: Circuit Analysis Manakula Vinayagar Institute of Technology	Member
4.	Mr. D. Balaji, Assistant Professor(SG) Dept of EEE Specialization: Machine design and Measurement and Control Manakula Vinayagar Institute of Technology	Member

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5.	Mr.D. Muruganandhan, Assistant Professor(SG) Dept of EEE Specialization: Power System Manakula Vinayagar Institute of Technology	Member
6.	Mr. J.Vijaya Raghavan, Assistant Professor Dept of EEE Manakula Vinayagar Institute of Technology	Member
7.	Mrs. R.Muthunagai, Assistant Professor Dept of EEE Manakula Vinayagar Institute of Technology	Member
8.	Mrs. R.Umamaheswari, Assistant Professor Dept of EEE Manakula Vinayagar Institute of Technology	Member
9.	Mrs.R.Priya, AP/EEE Assistant Professor(SG) Dept of EEE Manakula Vinayagar Institute of Technology	Member
10.	Mr.K.Rajesh, Assistant Professor Dept of EEE Manakula Vinayagar Institute of Technology	Member
11.	Mr.R.Goutham govind raju, Assistant Professor Dept of EEE Manakula Vinayagar Institute of Technology	Member
12.	Mr.S.Veeramanikandan, Assistant Professor Dept of EEE Manakula Vinayagar Institute of Technology	Member
13.	Dr.S.Sivaramakrishanan HOD/Dept of Mathematics, Manakula Vinayagar Institute of Technology	Member
14.	Dr.Sumithra ASP/Dept of Physics Manakula Vinayagar Institute of Technology	Member
15.	Mrs.J.Vinitha Mary Assistant Professor Dept.of IT, Manakula Vinayagar Institute of Technology	Member

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Two subject experts from outside the parent University are to be nominated by the Academic Council.

16.	Dr. Venkadesan .A Associate Professor/ EEE NIT Karaikal, Puducherry. venkadesan@nitpy.ac.in 9360609452	Subject Expert
17.	Dr.S.Baskar Dean (R&D)/EEE Thiagarajar College of Engineering baskar1970@gmail.com 9894039081	Subject Expert

One expert is to be nominated by the Vice-Chancellor from a panel of six recommended by the Autonomous College Principal.

18.	Dr.A.Kavitha Professor, Department of EEE College of Engineering Guindy Anna University, Chennai. 600 025 akavitha@annauniv.edu 94443887786	Subject Expert
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One representative from industry/corporate sector/allied areas to be nominated by the Principal.

19.	Mr.Raja Ganesan Director- Technical, VATIO Energy India Pvt Ltd, Chennai ganesh@vatioenergy.com 9566156789	Member
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One member of the College alumni to be nominated by the Principal.

20.	Mr. M.Abilash TCS – Automation Test Engineer, Bangalore. aabilash099@gmail.com 7010679629	Member
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Experts from outside the Autonomous College, whenever special courses of studies are to be formulated, to be nominated by the Principal.



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21.	Dr.Thanga Raj Chelliah, Associate Professor, Dept. of Water Resources Development and Management, IIT ROORKEE thanga.chelliah@wr.iitr.ac.in 9456593798	Member
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Agenda of the Meeting

Item No.	Particulars
BoS/ 3 / 2026 / EEE/UG 2.1	Progress and Activities of the EEE Department in A.Y 2025-26.
BoS/ 3 / 2026 / EEE /UG 2.2	Confirmation of minutes of 2 nd Board of Studies meeting.
BoS/ 3 / 2026 / EEE /UG 2.3	To discuss the proposed B.Tech Degree Curriculum structure of V semester under Autonomous Regulations (R2025) for the B.Tech – Electrical and Electronics Engineering students admitted in the Academic Year 2025-2026.
BoS/ 3 / 2026 / EEE /UG 2.4	To discuss the proposed curriculum structure for the V semester of the Honours and Minor Degree programmes offered by the Department of Electrical and Electronics Engineering, under the Autonomous Regulations (R2025) applicable to students admitted during the Academic Year 2025–2026.
BoS/ 3 / 2026 / EEE /UG 2.5	To discuss the initiatives undertaken by the Department for enhancing industry interaction, practical exposure, and employability skills among students
BoS/ 3 / 2026 / EEE /UG 2.6	To discuss measures for improving the overall student learning experience under the autonomous curriculum.
BoS/ 3 / 2026 / EEE /UG 2.7	To discuss and approve the panel of examiners list.
BoS/ 3 / 2026 / EEE /UG 2.8	Any other additional points to be discussed with the permission of chair

Dr. C. Shanmugasundaram, Chairman of the Board of Studies (BoS), formally commenced the meeting by extending a warm welcome to all external, internal, and co-opted members. He expressed his sincere gratitude for their acceptance of the invitation to serve on the Board and for their valuable presence. Following the

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welcome address, Dr.C. Shanmugasundaram presented a brief overview of the institution and the department, highlighting key achievements, major focus areas, and ongoing academic initiatives.

AGENDA 1: BoS/ 3 / 2026 / EEE /UG 2.1 Progress and Activities of the EEE Department in A.Y 2025-26.

The external members of the Board appreciated the academic results and the overall performance of the Department. They also appreciated the various academic and professional development initiatives organized by the Department, including guest lectures, industry visits, IPR awareness programmes, alumni activities, special lectures, workshops, and other skill-oriented programmes. The members acknowledged that these initiatives provide students with practical exposure, industry awareness, technical knowledge, and opportunities for holistic development.

AGENDA 2: BoS/ 3 / 2026 / EEE /UG 2.2 Confirmation of minutes of 2nd Board of Studies meeting.

The minutes of the second BoS meeting held on 25th July 2026 was confirmed in the meeting.

AGENDA 3: BoS/ 3 / 2025 / EEE /UG 2.3 To discuss the proposed B.Tech Degree Curriculum structure of V semester under Autonomous Regulations (R2025) for the B.Tech – Electrical and Electronics Engineering students admitted in the Academic Year 2025-2026.

The Head of the Department further reported that the Curriculum Structure and Syllabi for the III and IV Semesters under R2025 had already been presented, discussed, and approved by the BoS members during the Second Board of Studies Meeting. It was informed that the approved curriculum structure and syllabi for the III and IV Semesters would be continued and followed as approved, ensuring consistency and continuity in the implementation of the R2025 curriculum.

The proposed B.Tech Degree Curriculum Structure for the V Semester under the Autonomous Regulations (R2025) for the B.Tech – Electrical and Electronics Engineering students admitted in the Academic Year 2025–2026 were presented by the Head of the Department and discussed in detail. The Board deliberated on the course structure, credit distribution, core and elective courses, laboratory components, professional courses, and the overall academic requirements of the semesters. The suggestions of the members were noted for incorporation wherever applicable.



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AGENDA 4: BoS/ 3 / 2025 / EEE /UG 2.4 : To discuss the proposed curriculum structure for the V semester of the Honours and Minor Degree programmes offered by the Department of Electrical and Electronics Engineering, under the Autonomous Regulations (R2025) applicable to students admitted during the Academic Year 2025–2026.

The proposed curriculum structure for the V semester of the Honours and Minor Degree programmes offered by the Department of Electrical and Electronics Engineering, under the Autonomous Regulations (R2025), were presented and discussed in detail. The relevance of the proposed courses to current technological developments, industry expectations, higher education, and professional competencies was also discussed.

The following suggestions were given by the BoS members

- ❖ The members suggested avoiding duplication of topics across different courses and maintaining proper course continuity.
- ❖ The members recommended including recent software tools and simulation platforms wherever relevant.
- ❖ The members recommended including suitable industry-oriented topics, case studies, and real-time applications in the syllabi

The above comments were incorporated and the revised version B.Tech, R2025 Autonomous syllabi for the V semester will be approved in the next BoS Meeting.

AGENDA 5: BoS/ 3 / 2026 / EEE /UG 2.5 To discuss the initiatives undertaken by the Department for enhancing industry interaction, practical exposure, and employability skills among students

The Board members discussed the various initiatives undertaken by the Department to enhance industry interaction, practical exposure, placement readiness, and employability skills among students. The Department highlighted the placement training programmes, aptitude and technical training, soft-skill development sessions, mock interviews, industry visits, internships, guest lectures, workshops, and hands-on training activities organized for the students. The members also noted the participation of core Electrical and Electronics Engineering companies in the campus recruitment process, providing students with opportunities to pursue careers in their core domain. The Board appreciated these initiatives and suggested strengthening industry–academia interaction and placement-oriented training to further improve students' technical competencies, professional skills, and employment opportunities.



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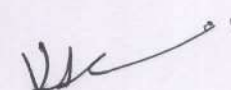
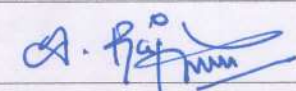
AGENDA 6: BoS/ 3 / 2026 / EEE /UG 2.6 To discuss measures for improving the overall student learning experience under the autonomous curriculum.

The discussion focused on strengthening student-centric teaching-learning practices, practical and experiential learning, project-based activities, industry interaction, skill development, and academic support. The need to provide opportunities for interdisciplinary learning, research, innovation, entrepreneurship, and professional development was also highlighted by the members. The members suggested that student feedback, academic performance, and learning outcomes may be regularly reviewed to identify areas for improvement and ensure a more engaging, flexible, and outcome-oriented learning environment.

AGENDA7: BoS/ 3 / 2026 / EEE /UG 2.7 To discuss and approve the panel of examiners list.

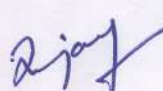
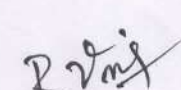
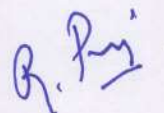
The Board discussed and approved the proposed panel of examiners for the forthcoming academic examinations. The panel was reviewed in accordance with the university norms and departmental requirements, and was subsequently recommended for onward submission to the Academic Council.

The meeting was concluded at 1.00 PM with vote of thanks by Dr.C. Shanmugasundaram, Chairman, Board of Studies, Department of Electrical and Electronics Engineering, Manakula Vinayagar Institute of Technology, Puducherry.

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The entire faculty of each specialization			
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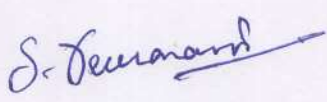
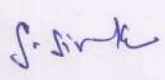
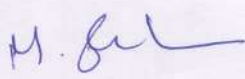
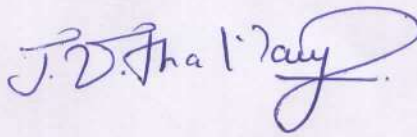
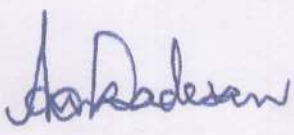
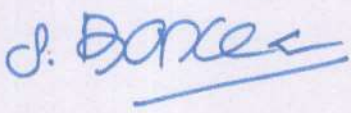
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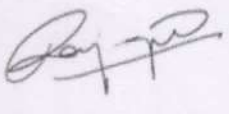
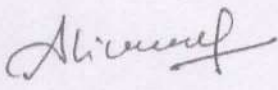


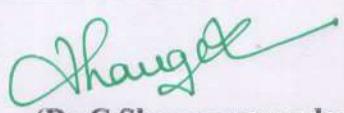
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12.	Mr.S.Veeramanikandan, Assistant Professor Dept of EEE Manakula Vinayagar Institute of Technology		
13.	Dr. Sivaramakrishnan HOD/Dept of Mathematics, Manakula Vinayagar Institute of Technology	Member	
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One member of the College alumni to be nominated by the Principal.			
20.	Mr. M.Abilash TCS – Automation Test Engineer, Bangalore. aabilash099@gmail.com 7010679629	Member	
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(Dr.C.Shanmugasundaram)

Dr. C. SHANMUGASUNDARAM, BE, M.Tech, Ph.D.
Chairperson-BOS

PROFESSOR AND HEAD

Department of Electrical and Electronics Engineering, MIT

Manakula Vinayagar Institute of Technology

Kalitheerthalkunnam, Puducherry-605 107.

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