


3.1.2 Number of teachers recognized as research guides for the year 2023-24

3.3.1 Number of Ph.Ds registered per eligible teacher during the year 2023-24

Name of full time teacher with Ph.D./D.M/M.Ch./D.N.B Superspeciality/D. Sc./D' Lit.	Qualification (Ph.D./D.M/M.Ch./D.N.B Superspeciality/D.Sc./D' Lit.) and Year of obtaining	Whether recognised as research Guide for Ph.D./D.M/M.Ch./D.N.B Superspeciality/D. Sc./D' Lit.	Year of Recognition as Research Guide	Is the teacher still serving the institution/If not last year of the service of Faculty to the Institution	Name of the scholar	Year of registration of the scholar	Title of the thesis for scholar
Dr.S.Malarkkan	Ph.D,2008	Ph.D	2023	yes	Edara Bharat Babu	2023	Development of Acoustic Source Localization using Deep Neural Networks
Dr.R.Valli	Ph.D, 2012	Ph.D	2023	yes	Krishna Kishore Gudimella	2023	Investigation on Design of Antennas for OAM beam generation and its performance analysis
Dr.S.Arunmozhi	Ph.D, 2019	Ph.D	2023	yes	Bhavana Godavarthi	2023	Advanced FCNN-Based Image Denoising for Medical Imaging: Integrating Residual Learning and Feature Extraction Techniques
Dr.S.Vinoth	Ph.D, 2018	Ph.D	2023	yes	Paparao Nalajala	2023	Advanced LGRP Featured based Efficient Classification of Soil by using Quantum Convolutional Neural Networks (QCNN)


 PRINCIPAL
 MANAKULA VINAYAGAR
 INSTITUTE OF TECHNOLOGY,
 KALITHEERTHALKUPPAM,
 PUJUCHERRY - 605 107.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING

Dr. J. SAMPATH. M.A., B.L., Ph.D.,

University Campus, Kalapet
Pudhucherry -605 014
Phone (0413)-2655176

Controller of Examinations.

PU/CE/E9/PHD/2012-13/

Dated: 17.04.2013

Dr. S. Malarkkan TPC
Professor of ECE
Principal, Manakula Vinayagar Institute of Technology
Madagadipet,
Puducherry - 605 107.

Sir,

SUB: Your request for Recognition as Supervisor for
Guiding Ph.D. Scholars - Reg.

I am, by direction of the Vice Chancellor, to inform that you have been recognized as a Supervisor to guide the research work of candidates leading to Ph.D. Degree, subject to the ratification of the Academic Council.

You may kindly note that under the Revised Regulations of this University, each Supervisor is permitted to guide not more than 8 Research Scholars at any given time and this maximum number includes full time and part time (Internal and External) Research Scholars. In exceptional cases, the Vice Chancellor is empowered to decide.

A copy of the revised Ph.D. Regulations may be downloaded from our website for your information and guidance.

**FIELD OF SPECIALISATION/SUBJECT: Electronic and
Communication Engineering**

Yours faithfully,

CONTROLLER OF EXAMINATIONS

Copy to:

1. The Dy.Registrar(Aca-II) for obtaining approval / ratification of the Academic Council.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING

Dr. S. Murugaiyan, PhD
Assistant Registrar (Research)

Kalapet, Puducherry -605 014.
Office: 0413-2654819.

स/PU/CE/PHD/2023/Rs-3/ 104

दिनांक/Date:25.09.2023.

To;
Dr. R. Valli,
Professor,
Department of ECE,
Manakula Vinayagar Institute of Technology,
Puducherry 605 107.
Sir/Madam,

SUB: Recognition as Supervisor for Guiding Ph.D. Scholars – Reg.

Ref: Lr. No: PU/DEAN/SET/2023/1108, dated: 14.07.2023.

I am by direction to inform that you have been recognized as a Supervisor to guide the research work of candidates leading to Ph.D. Degree, subject to the ratification of the Academic Council.

You may kindly note that under the Revised Ph.D. Regulations of this University, each Supervisor is permitted to guide Research Scholars and the maximum number includes full time and part time (Internal and External) Research Scholars (as per revised UGC Regulations).

A Supervisor, who will be superannuating in less than **03** years period, cannot take a new Ph.D. student. However, he/she can co-supervise new students.

A copy of the revised Ph.D. Regulations may be downloaded from our website for your information and guidance.

FIELD OF SPECIALIZATION: WIRELESS COMMUNICATION

Yours faithfully,


Assistant Registrar (Research)

Copy to:

1. The Dean, School of Engineering and Technology, Pondicherry University.
2. The Principal, Manakula Vinayagar Institute of Technology, Puducherry.
3. The Assistant Registrar, Academic Section, Pondicherry University.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING

Dr. S. Murugaiyan, PhD
Assistant Registrar (Research)

Kalapet, Puducherry -605 014.
Office: 0413-2654819.

सं/PU/CE/PHD/2023/Rs-3/103

दिनांक/Date: 25.09.2023.

To;
Dr. S. Arunmozhi,
Professor,
Department of ECE,
Manakula Vinayagar Institute of Technology,
Puducherry 605 107.

Sir/Madam,

SUB: Recognition as Supervisor for Guiding Ph.D. Scholars – Reg.

Ref: Lr. No: PU/DEAN/SET/2023/1108, dated: 14.07.2023.

I am by direction to inform that you have been recognized as a Supervisor to guide the research work of candidates leading to Ph.D. Degree, subject to the ratification of the Academic Council.

You may kindly note that under the Revised Ph.D. Regulations of this University, each Supervisor is permitted to guide Research Scholars and the maximum number includes full time and part time (Internal and External) Research Scholars (as per revised UGC Regulations).

A Supervisor, who will be superannuating in less than **03** years period, cannot take a new Ph.D. student. However, he/she can co-supervise new students.

A copy of the revised Ph.D. Regulations may be downloaded from our website for your information and guidance.

FIELD OF SPECIALIZATION: WIRELESS COMMUNICATION

Yours faithfully,


Assistant Registrar (Research)

Copy to:

1. The Dean, School of Engineering and Technology, Pondicherry University.
2. The Principal, Manakula Vinayagar Institute of Technology, Puducherry.
3. The Assistant Registrar, Academic Section, Pondicherry University.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING



Dr. S. Murugaiyan, PhD
Assistant Registrar (Research)

Kalapet, Puducherry -605 014.
Office: 0413-2654819.

सं/PU/CE/PHD/2023/Rs-3/105

दिनांक/Date: 25.09.2023.

To;
Dr. S. Vinoth,
Assistant Professor,
Department of ECE,
Manakula Vinayagar Institute of Technology,
Puducherry 605 107.
Sir/Madam,

SUB: Recognition as Supervisor for Guiding Ph.D. Scholars – Reg.
Ref: Lr. No: PU/DEAN/SET/2023/1108, dated: 14.07.2023.

I am by direction to inform that you have been recognized as a Supervisor to guide the research work of candidates leading to Ph.D. Degree, subject to the ratification of the Academic Council.

You may kindly note that under the Revised Ph.D. Regulations of this University, each Supervisor is permitted to guide Research Scholars and the maximum number includes full time and part time (Internal and External) Research Scholars (as per revised UGC Regulations).

A Supervisor, who will be superannuating in less than **03** years period, cannot take a new Ph.D. student. However, he/she can co-supervise new students.

A copy of the revised Ph.D. Regulations may be downloaded from our website for your information and guidance.

FIELD OF SPECIALIZATION: NANO TECHNOLOGY

Yours faithfully,

Assistant Registrar (Research)

Copy to:

1. The Dean, School of Engineering and Technology, Pondicherry University.
2. The Principal, Manakula Vinayagar Institute of Technology, Puducherry.
3. The Assistant Registrar, Academic Section, Pondicherry University.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING

SADANAND G SWAMI
Controller of Examinations

Kalapet, Puducherry -605 014.
Office: 0413-2654204.

PU/CE/PHD/2023-24/R2/720

Date: 20.03.2024

MEMORANDUM

Sub: Provisional Registration of Ph.D. Programme - Reg.

Mr. Bharat Babu E., Reg. No. 20233138001 has been provisionally registered for the Ph.D. Programme in **Electronics and Communication Engineering (Part-Time, External)** with effect from **19/12/2023**.

The Supervisor may constitute the Doctoral Committee through the Dean of the Concerned School of our University as per Ph.D. Regulation. A copy of the Doctoral Committee members list be sent to the Controller of Examinations. The Report of the Doctoral Committee should be submitted every six months till the submission of thesis along with a copy of the semester fees paid.

The candidate should work for a minimum total period of two years under the supervision of the Guide. Since the candidate has qualified M.Tech degree, the candidate is exempted from appearing for the Part-I Examination of the Ph.D. Programme and it is **Confirmed** to proceed further with the Research Work. However, if he/she have not completed courses on (1) Research Methodology and (2) Research and Publication Ethics (RPE) at M.Phil / M.Tech., he/she will be required to complete these two courses under Part-I of Ph.D. Programme.

The candidate shall publish minimum of one research paper in standard peer reviewed journal and present at least one in National/International level Seminars/Conference/Workshop before submitting the thesis. After completion of three years, the candidate may submit the Thesis and the maximum period for submission of Thesis is **6 years**.

The Head of the Department may kindly ensure that the number of students guided by the supervisor is in accordance with Ph.D. Regulation in force. The candidate will work under the guidance of **Dr. S. Malarkkan**, Professor in Electronics and Communication Engineering, Manakula Vinayagar Institute of Technology, Puducherry in the area of **Acoustic Source Localization (Signal Processing)**.

Controller of Examinations

To
Mr. Bharat Babu E., Reg. No. 20233138001 ...TPC,
Ph.D. Scholar in Electronics and Communication Engineering (Part-Time, External),
Manakula Vinayagar Institute of Technology,
Kalitheerthalkuppam, Puducherry – 605 107.

- Copy to:**
1. The Dean, School of Engineering and Technology, PU.
 2. The Principal, Manakula Vinayagar Institute of Technology, Puducherry.
 3. Dr. S. Malarkkan, Professor in ECE, MIT, Puducherry.
 4. The Assistant Registrar (Academic section) - for kind information.

Note:

1. The details of scholarship/stipend availed by the candidate should be enclosed along with the first D.C. minutes.
2. Affiliated Research Institutions should pay Rs. 25,000/- (Rupees Twenty Five Thousand Only) towards remuneration to the Examiners and postal charges by means of D.D. in favour of Finance Officer, Pondicherry University.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING

SADANAND G SWAMI
Controller of Examinations

Kalapet, Puducherry -605 014.
Office: 0413-2654204.

PU/CE/PHD/2023-24/R2/79

Date: 20.03.2024

MEMORANDUM

Sub: Provisional Registration of Ph.D. Programme - Reg.

Mr. Krishnakishore Gudimella, Reg. No. 20233138002 has been provisionally registered for the Ph.D. Programme in **Electronics and Communication Engineering (Part-Time, External)** with effect from **19/12/2023**.

The Supervisor may constitute the Doctoral Committee through the Dean of the Concerned School of our University as per Ph.D. Regulation. A copy of the Doctoral Committee members list be sent to the Controller of Examinations. The Report of the Doctoral Committee should be submitted every six months till the submission of thesis along with a copy of the semester fees paid.

The candidate should work for a minimum total period of two years under the supervision of the Guide. Since the candidate has qualified M.Tech degree, the candidate is exempted from appearing for the Part-I Examination of the Ph.D. Programme and it is **Confirmed** to proceed further with the Research Work. However, if he/she have not completed courses on (1) Research Methodology and (2) Research and Publication Ethics (RPE) at M.Phil / M.Tech., he/she will be required to complete these two courses under Part-I of Ph.D. Programme.

The candidate shall publish minimum of one research paper in standard peer reviewed journal and present at least one in National/International level Seminars/Conference/Workshop before submitting the thesis. After completion of three years, the candidate may submit the Thesis and the maximum period for submission of Thesis is **6 years**.

The Head of the Department may kindly ensure that the number of students guided by the supervisor is in accordance with Ph.D. Regulation in force. The candidate will work under the guidance of **Dr. R. Valli**, Professor in Electronics and Communication Engineering, Manakula Vinayagar Institute of Technology, Puducherry in the area of **Wireless Body Area Networks, VLSI Design**.


Controller of Examinations

To
Mr. Krishnakishore Gudimella, Reg. No. 20233138002 ...TPC,
Ph.D. Scholar in Electronics and Communication Engineering (Part-Time, External),
Manakula Vinayagar Institute of Technology,
Kalitheerthalkuppam, Puducherry – 605 107.

- Copy to:**
1. The Dean, School of Engineering and Technology, PU.
 2. The Principal, Manakula Vinayagar Institute of Technology, Puducherry.
 3. Dr. R. Valli, Professor in ECE, MIT, Puducherry.
 4. The Assistant Registrar (Academic section) - for kind information.

Note:

1. The details of scholarship/stipend availed by the candidate should be enclosed along with the first D.C. minutes.
2. Affiliated Research Institutions should pay Rs. 25,000/- (Rupees Twenty Five Thousand Only) towards remuneration to the Examiners and postal charges by means of D.D. in favour of Finance Officer, Pondicherry University.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING

SADANAND G SWAMI
Controller of Examinations

Kalapet, Puducherry -605 014.
Office: 0413-2654204.

PU/CE/PHD/2023-24/R2/718

Date: 20.03.2024

MEMORANDUM

Sub: Provisional Registration of Ph.D. Programme - Reg.

Ms. Bhavana Godavarthi, Reg. No. 20233138003 has been provisionally registered for the Ph.D. Programme in **Electronics and Communication Engineering (Part-Time, External)** with effect from **19/12/2023**.

The Supervisor may constitute the Doctoral Committee through the Dean of the Concerned School of our University as per Ph.D. Regulation. A copy of the Doctoral Committee members list be sent to the Controller of Examinations. The Report of the Doctoral Committee should be submitted every six months till the submission of thesis along with a copy of the semester fees paid.

The candidate should work for a minimum total period of two years under the supervision of the Guide. Since the candidate has qualified M.Tech degree, the candidate is exempted from appearing for the Part-I Examination of the Ph.D. Programme and it is **Confirmed** to proceed further with the Research Work. However, if he/she have not completed courses on (1) Research Methodology and (2) Research and Publication Ethics (RPE) at M.Phil / M.Tech., he/she will be required to complete these two courses under Part-I of Ph.D. Programme.

The candidate shall publish minimum of one research paper in standard peer reviewed journal and present at least one in National/International level Seminars/Conference/Workshop before submitting the thesis. After completion of three years, the candidate may submit the Thesis and the maximum period for submission of Thesis is **6 years**.

The Head of the Department may kindly ensure that the number of students guided by the supervisor is in accordance with Ph.D. Regulation in force. The candidate will work under the guidance of **Dr. S. Arunmozhi**, Professor in Electronics and Communication Engineering, Manakula Vinayagar Institute of Technology, Puducherry in the area of **Residual Learning Based Image Denoiser for Medical Images using FCNN**.


Controller of Examinations

To

Ms. Bhavana Godavarthi, Reg. No. 20233138003 ...TPC,
Ph.D. Scholar in Electronics and Communication Engineering (Part-Time, External),
Manakula Vinayagar Institute of Technology,
Kalitheerthalkuppam, Puducherry – 605 107.

- Copy to:**
1. The Dean, School of Engineering and Technology, PU.
 2. The Principal, Manakula Vinayagar Institute of Technology, Puducherry.
 3. Dr. S. Arunmozhi, Professor in ECE, MIT, Puducherry.
 4. The Assistant Registrar (Academic section) - for kind information.

Note:

1. The details of scholarship/stipend availed by the candidate should be enclosed along with the first D.C. minutes.
2. Affiliated Research Institutions should pay Rs. 25,000/- (Rupees Twenty Five Thousand Only) towards remuneration to the Examiners and postal charges by means of D.D. in favour of Finance Officer, Pondicherry University.



PONDICHERRY UNIVERSITY
(Central University)
EXAMINATION WING

SADANAND G SWAMI
Controller of Examinations

Kalapet, Puducherry -605 014.
Office: 0413-2654204.

PU/CE/PHD/2023-24/R2/717

Date: 20.03.2024

MEMORANDUM

Sub: Provisional Registration of Ph.D. Programme - Reg.

Mr. Paparao Nalajala, Reg. No. 20233138004 has been provisionally registered for the Ph.D. Programme in **Electronics and Communication Engineering (Part-Time, External)** with effect from **19/12/2023**.

The Supervisor may constitute the Doctoral Committee through the Dean of the Concerned School of our University as per Ph.D. Regulation. A copy of the Doctoral Committee members list be sent to the Controller of Examinations. The Report of the Doctoral Committee should be submitted every six months till the submission of thesis along with a copy of the semester fees paid.

The candidate should work for a minimum total period of two years under the supervision of the Guide. Since the candidate has qualified M.Tech degree, the candidate is exempted from appearing for the Part-I Examination of the Ph.D. Programme and it is **Confirmed** to proceed further with the Research Work. However, if he/she have not completed courses on (1) Research Methodology and (2) Research and Publication Ethics (RPE) at M.Phil / M.Tech., he/she will be required to complete these two courses under Part-I of Ph.D. Programme.

The candidate shall publish minimum of one research paper in standard peer reviewed journal and present at least one in National/International level Seminars/Conference/Workshop before submitting the thesis. After completion of three years, the candidate may submit the Thesis and the maximum period for submission of Thesis is **6 years**.

The Head of the Department may kindly ensure that the number of students guided by the supervisor is in accordance with Ph.D. Regulation in force. The candidate will work under the guidance of **Dr. S. Vinoth**, Assistant Professor in Electronics and Communication Engineering, Mankula Vinayagar Institute of Technology, Puducherry in the area of **Image Processing**.


Controller of Examinations

To
Mr. Paparao Nalajala, Reg. No. 20233138004 ...TPC,
Ph.D. Scholar in Electronics and Communication Engineering (Part-Time, External),
Manakula Vinayagar Institute of Technology,
Kalitheerthalkuppam, Puducherry – 605 107.

- Copy to:**
1. The Dean, School of Engineering and Technology, PU.
 2. The Principal, Manakula Vinayagar Institute of Technology, Puducherry.
 3. Dr. S. Vinoth, Assistant Professor in ECE, MIT, Puducherry.
 4. The Assistant Registrar (Academic section) - for kind information.

Note:

1. The details of scholarship/stipend availed by the candidate should be enclosed along with the first D.C. minutes.
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