Name :   **V.Kumaraguru**
Designation: **Assistant Professor**
Department: **CSE(IoT & CS)**
Specialization: VLSI DESIGN
Experience: **11 Year 10 Months**
E-mail ID: Kumaragurucse@Gmail.com
Research IDs: -

Academic Qualifications:

Examination	Institute / University	Year	Subjects
Ph.D.	-	-	-
M.E	Government College of Technology, Coimbatore	2012	VLSI DESIGN
B.E	V.R.S College of Engineering	2008	CSE

Details of Employment Experience: (Starting with the most recent)

Job Title	Employer	From	To
Assistant Professor	E.S Engineering College, Villupuram	18.06.2012	20.09.2021
Assistant Professor	Manakula Vinayagar Institute of Technology, Puducherry	11.10.2021	Till date

Academic/Administrative Responsibilities:

Position	Department/Cell/Institution	From	To
Class Advisor	III CSE(IoT & CS)	August 2022	till date
Dept.Exam cell Coordinator	CSE(IoT & CS)	Nov 2021	till date
NPTEL Coordinator	CSE(IoT & CS)	Nov 2021	till date
IQAC Coordinator	CSE(IoT & CS)	Nov 2021	till date
Time Table Coordinator	CSE(IoT & CS)	Nov 2021	till date
Department Association Coordinator	CSE(IoT & CS)	Nov 2021	till date
SRC Executive member	CSE(IoT & CS)	JUN-2023	till date

Research Contributions:

No.of Publications				No.of Patents			No. of Funds Sanctioned	
International Journal	National Journal	International Conference	National Conference	Filed	Published	Granted	FDP/Workshop /Seminar	Funded Projects
1	-	-	-	1	1	0	-	-

Details of Academic Achievements, Awards and Recognitions:

1. Received an appreciation award for 100% for Subject of Big Data of the batch 2020-2024